Communication apparatus between several processors

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Inventor(s): CORNILLON PIERRE [FR] +
Applicant(s): CIT ALCATEL [FR] +
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Abstract of **EP 0910021 (A1)**

The system uses specific routing devices to enable selection of local and remote memory requests to increase overall processing speed. Several processors (P11,P12,P13..) may be situated on different circuit boards (C1, C2, C3 ..). Each processor is connected by a local bus (BL1, BL2, BL3 ..) to a memory (M1, M2, M3 ..). For each processor there is a routing device (RC1) connected to the local bus and connected to an external bus (BE) connecting all the cards. The routing device analyses the access requests transmitted on the local bus, and determines whether data should be applied to the local memory or sent in packets on the external bus. It also analyses the packets transmitted on the external bus, and transmits a request to the local memory if a packet received on the external bus has a request for access to the local memory.

FIG.1